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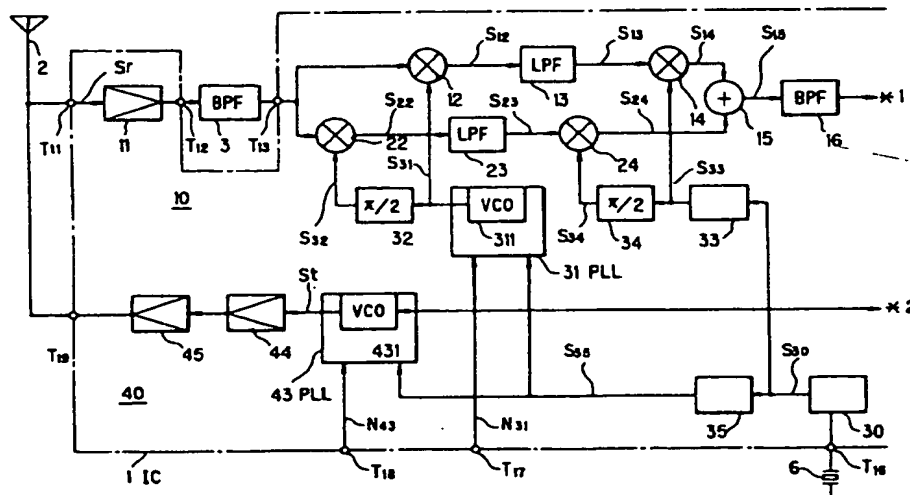
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(54) Transmitting and receiving apparatus for incorporation into an integrated circuit

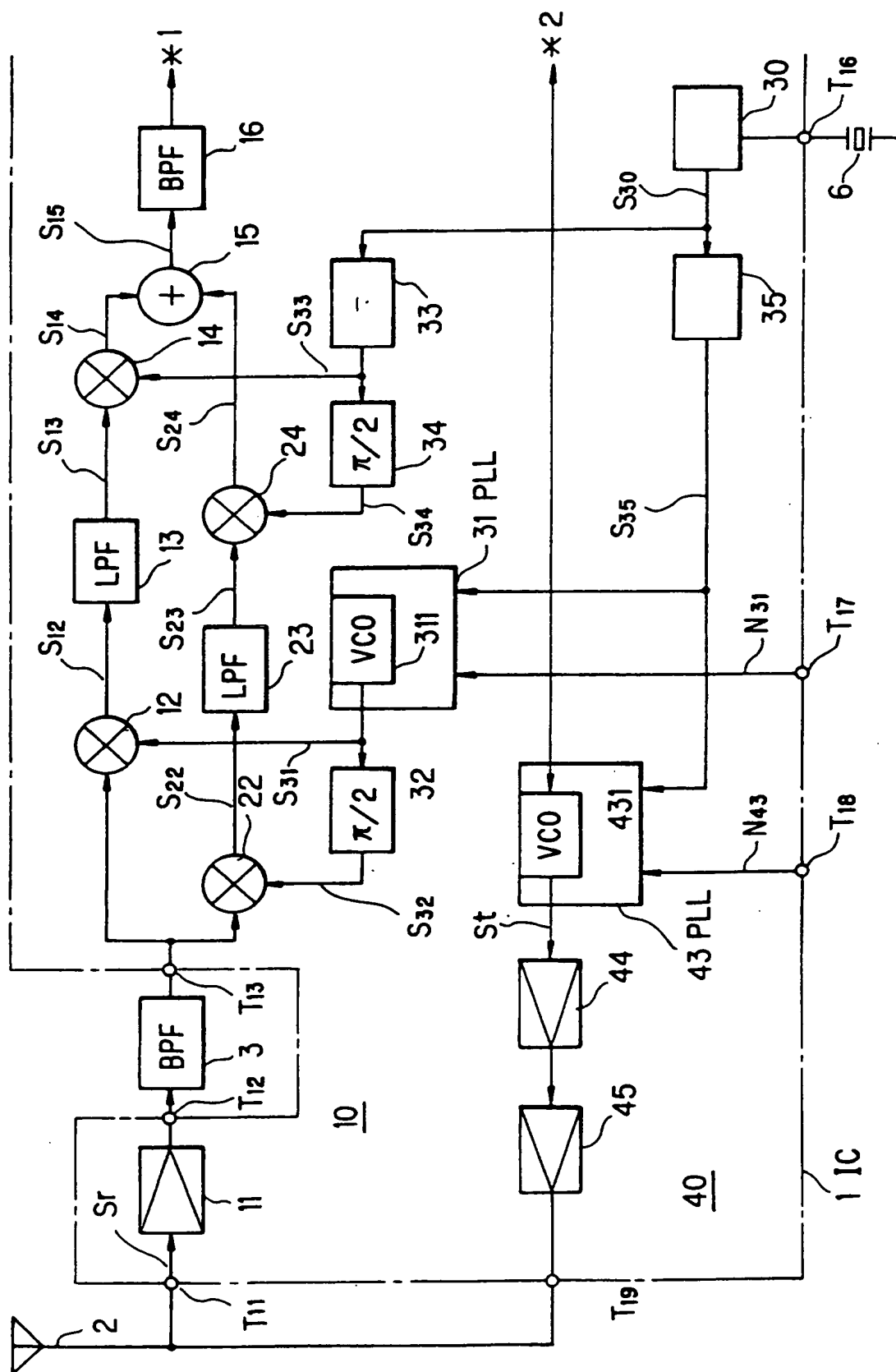
(57) A transmitting and receiving apparatus configured in superheterodyne form, includes a receiving circuit for receiving a signal on one channel of a predetermined pair of channels and a transmitting circuit for transmitting a signal on the other channel of the predetermined pair of channels. The receiving circuit has a first voltage-controlled oscillator 311 including a first oscillating transistor and generating a local oscillated signal used for receiving the signal on the one of the channels, and a first current control means ( $R_{11}$ ,  $Q_{11}$ , Fig. 6) for controlling a collector current of the first oscillating transistor. The transmitting circuit has a second voltage-controlled oscillator 431 including a second oscillating transistor and generating a signal of the frequency of the other channel, and a second current control means ( $Q_{12}$ ,  $R_{11}$ ,  $Q_{11}$ , Fig. 6) for controlling a collector current of the second oscillating transistor. By suitable control of the current control means, the frequencies of the oscillators 311, 431 may be switched over so that the same integrated circuit including the receiving, transmitting circuits may be used in the base station circuitry as well as in the mobile or portable unit.

FIG. 1



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FIG. 1





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FIG. 2

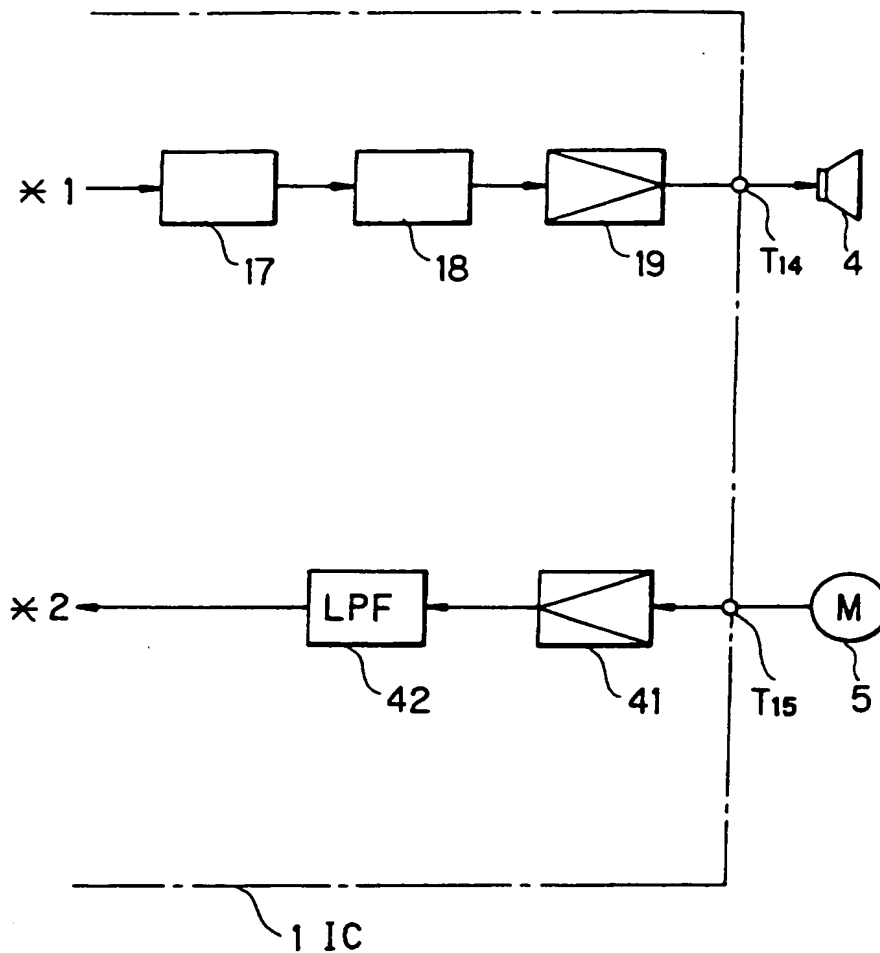


FIG. 3A

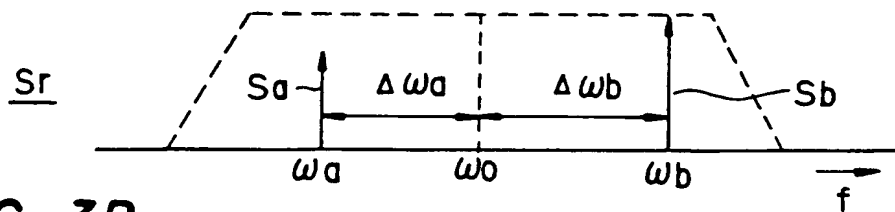
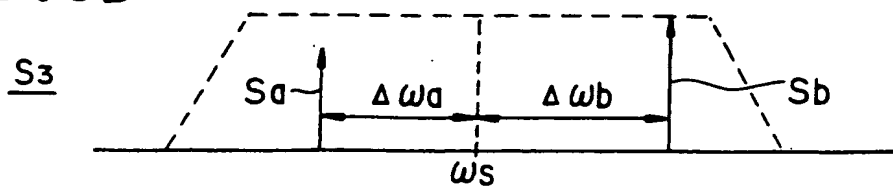


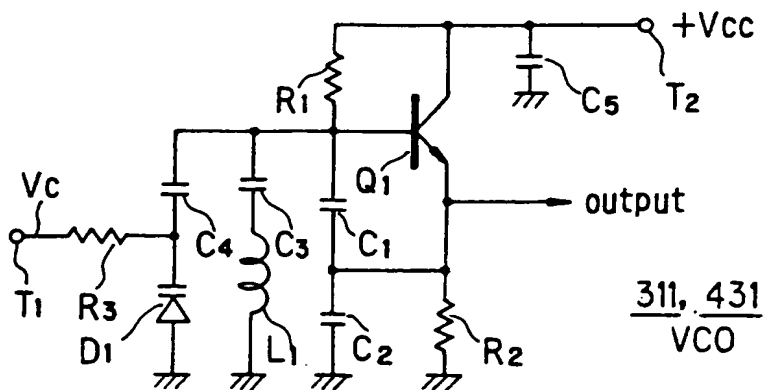
FIG. 3B



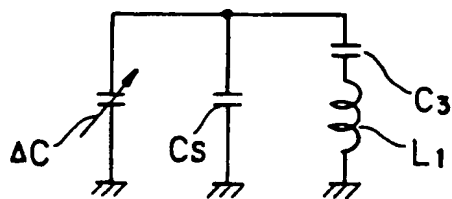


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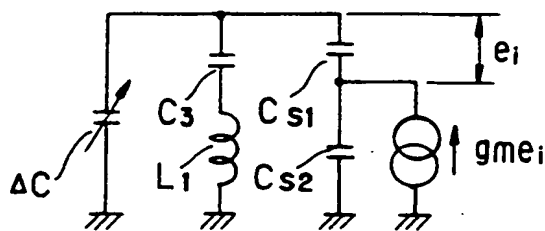
**FIG. 4**



**FIG. 5A**



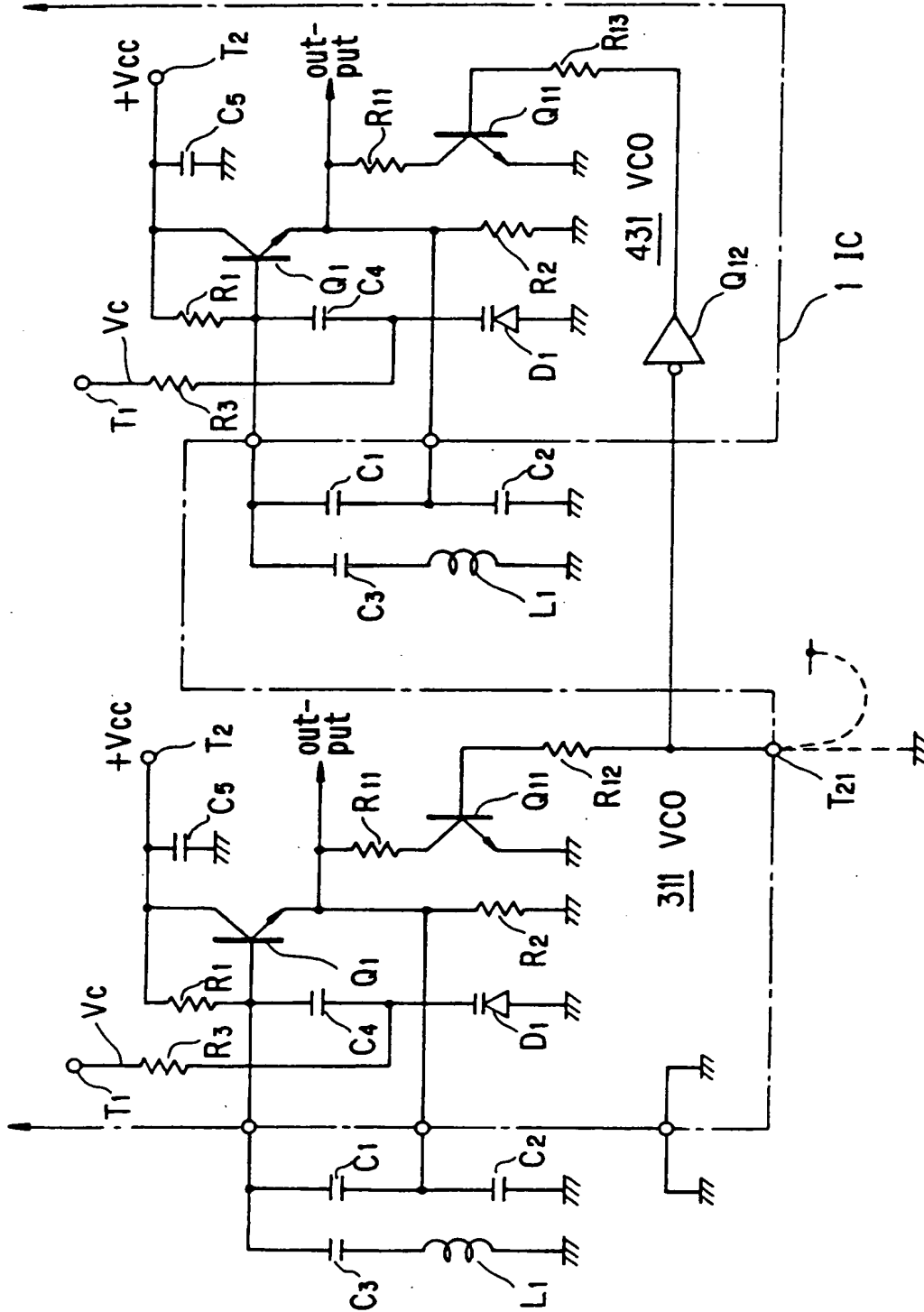
**FIG. 5B**







**FIG. 6**





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TRANSMITTING AND RECEIVING APPARATUS  
FOR INCORPORATION INTO AN INTEGRATED CIRCUIT

The present invention relates to the incorporation on a single chip of a suitable signal transmitting/receiving circuit.

The channel frequencies for low power-type cordless telephones in Japan are as follows.

up channel (portable unit to base unit)....381 MHz band  
down channel (base unit to portable unit)..254 MHz band  
channel spacing.....12.5 kHz.

The circuits shown, for example, in FIG. 1 and FIG. 2 may be considered for cordless telephone receiving and transmitting circuits. These circuits correspond to the above standard, may be incorporated on a single chip, and this IC may be shared by the base unit and portable unit.

Namely, \*1 and \*2 of FIG. 1 and \*1 and \*2 of FIG. 2 are connected and the portion surrounded by the chain line is incorporated in a single chip. These figures show the IC1 used for the portable unit with the receiving and transmitting circuits respectively designated with reference numerals 10 and 40.

The receiving circuit 10 has a direct conversion-type structure employing a double super-heterodyning method. i.e. a down channel FM signal  $S_r$  from the base unit is

received by the antenna 2 and then provided from a terminal T11 to a frequency amplifier 11, on to a terminal T12, then to a band pass filter 3 that allows all of the down channels to pass and then finally via terminal T13 on to first mixer circuits 12 and 22 for performing an orthogonal transformation to an I axis and a Q axis.

Further, an oscillating circuit 30 generates a stabilized reference frequency such as an oscillating frequency of, for example, 14.4 MHz. A crystal oscillating circuit 6 is therefore connected via the terminal T16.

This oscillating signal S30 of the oscillating circuit 30 is then provided to a dividing circuit 35 and divided to a frequency of  $1/1152$  i.e. the signal S35 of channel spacing frequency 12.5kHz. This signal S35 is then provided to a PLL 31 as the reference frequency signal, with the dividing ratio N31 being provided to and set-up at a variable dividing circuit (not shown in the drawings) of the PLL 31 via a terminal T17.

An oscillating frequency S31 of a frequency equal to the carrier frequency of the FM signal  $S_r$  is then taken out from the VCO311 of the PLL31.

This signal S31 is then provided to the mixer circuit 12 as a first local oscillating signal before being provided to a phase-shifting circuit 32, where the signal S31 is phase-shifted by just  $\pi/2$ , with this phase-shifted signal S32 then being provided to the mixer 22 as the first

local oscillating signal.

Therefore, as shown in FIG. 3A, the receiving signal  $S_r$  has a signal component  $S_a$  within the lower side waveband and a signal component  $S_b$  within the upper side waveband.

Further, taking

$\omega_o$  : the carrier frequency (angular frequency) of the received signal  $S_r$

$\omega_a$  : angular frequency of signal component  $S_a$ .  $\omega_a < \omega_o$

$E_a$  : amplitude of signal component  $S_a$

$\omega_b$  : angular frequency of signal component  $S_b$ .  $\omega_b < \omega_o$

$E_b$  : amplitude of signal component  $S_b$

$$\Delta\omega_a = \omega_o - \omega_a$$

$$\Delta\omega_b = \omega_b - \omega_o,$$

then,

$$S_r = S_a + S_b$$

$$S_a = E_a \cdot \sin\omega_a t$$

$$S_b = E_b \cdot \sin\omega_b t.$$

Further, taking

$E_1$  : the amplitude of the first local oscillating signals  $S_{31}$  and  $S_{32}$ ,

then

$$S_{31} = E_1 \cdot \sin\omega_o t$$

$$S_{32} = E_1 \cdot \cos\omega_o t.$$

Therefore, taking

$S_{12}$ ,  $S_{22}$  : output signals of the mixer circuits 12 and 22,

then,

$$S_{12} = S_r \cdot S_{31}$$

$$\begin{aligned} &= (E_a \cdot \sin \omega_a t + E_b \cdot \sin \omega_b t) \times E_1 \cdot \sin \omega_o t \\ &= \alpha_a \{-\cos(\omega_a + \omega_o)t + \cos(\omega_o - \omega_a)t\} \\ &+ \alpha_b \{-\cos(\omega_b + \omega_o)t + \cos(\omega_b - \omega_o)t\} \\ &= \alpha_a \{-\cos(\omega_a + \omega_o)t + \cos \Delta \omega_a t\} \\ &+ \alpha_b \{-\cos(\omega_b + \omega_o)t + \cos \Delta \omega_b t\} \end{aligned}$$

$$S_{22} = S_r \cdot S_{32}$$

$$\begin{aligned} &= (E_a \cdot \sin \omega_a t + E_b \cdot \sin \omega_b t) \times E_1 \cdot \cos \omega_o t \\ &= \alpha_a \{\sin(\omega_a + \omega_o)t - \sin(\omega_o - \omega_a)t\} \\ &+ \alpha_b \{\sin(\omega_b + \omega_o)t - \sin(\omega_b - \omega_o)t\} \\ &= \alpha_a \{\sin(\omega_a + \omega_o)t - \sin \Delta \omega_a t\} \\ &+ \alpha_b \{\sin(\omega_b + \omega_o)t - \sin \Delta \omega_b t\} \end{aligned}$$

wherein

$$\alpha_a = E_a \cdot E_1 / 2$$

$$\alpha_b = E_b \cdot E_1 / 2.$$

The signals  $S_{12}$  and  $S_{22}$  are provided to the low pass filters 13 and 23 since the signal components with the angular frequencies  $\Delta \omega_a$  and  $\Delta \omega_b$  are necessary for an intermediate frequency (hereinafter referred to as IF) signal. The signal components with the angular frequencies  $\Delta \omega_a$  and  $\Delta \omega_b$  are provided as the first IF signals  $S_{13}$  and  $S_{23}$  from the low pass filter 13. The signals  $S_{13}$  and  $S_{23}$  are expressed as follows:

$$S_{13} = \alpha_a \cdot \cos \Delta \omega_a t + \alpha_b \cdot \cos \Delta \omega_b t$$

$$S_{23} = -\alpha_a \cdot \sin \Delta \omega_a t + \alpha_b \cdot \sin \Delta \omega_b t.$$

In this case, as being apparent from the above

equations and FIG. 3A, the signals S13 and S23 are baseband signals.

These signals S13 and S23 are provided to the second mixer circuits 14 and 24 for I axis and Q axis of orthogonal transformation.

The oscillated signal S30 from the oscillating circuit 30 is provided to the frequency dividing circuit 33 and divided into a relatively low frequency signal S33. For example, the signal S33 is divided by 262 to a frequency of about 55kHz. This signal S33 is provided to the mixer 14 as the second local oscillated signal and provided to the phase shifting circuit 34. The phase-shifting circuit 34 phase-shifts this signal S33 by  $\pi/2$ . The phase-shifted signal S34 is provided to the mixer 24 as the second local oscillated signal.

Therefore, taking

$$S33 = E2 \cdot \sin \omega s t$$

$$S34 = E2 \cdot \cos \omega s t$$

wherein

E2 : amplitude of second local oscillating signals S33 and S34

$$\omega s = 2\pi f s$$

(fs = approximately 55 kHz)

and taking:

S14, S24 : output signals from the mixers 14 and 24,

then,

$$S_{14} = S_{13} \cdot S_{33}$$

$$= (\alpha_a \cdot \cos \Delta \omega a t + \alpha_b \cdot \cos \Delta \omega b t) \times E_2 \cdot \sin \omega s t$$

$$= \beta_a \{ \sin(\Delta \omega a + \omega s) t - \sin(\Delta \omega a - \omega s) t \}$$

$$+ \beta_b \{ \sin(\Delta \omega b + \omega s) t - \sin(\Delta \omega b - \omega s) t \}$$

$$S_{24} = S_{23} \cdot S_{34}$$

$$= (-\alpha_a \cdot \sin \Delta \omega a t + \alpha_b \cdot \sin \Delta \omega b t) \times E_2 \cdot \cos \omega s t$$

$$= -\beta_a \{ \sin(\Delta \omega a + \omega s) t + \sin(\Delta \omega a - \omega s) t \}$$

$$+ \beta_b \{ \sin(\Delta \omega b + \omega s) t + \sin(\Delta \omega b - \omega s) t \}$$

wherein

$$\beta_a = \alpha_a \cdot E_2 / 2$$

$$\beta_b = \alpha_b \cdot E_2 / 2.$$

The equations for signals  $S_{14}$  and  $S_{24}$  are transformed so that the value for the frequency difference does not become negative,

$$S_{14} = \beta_a \{ \sin(\Delta \omega a + \omega s) t + \sin(\omega s - \Delta \omega a) t \}$$

$$+ \beta_b \{ \sin(\Delta \omega b + \omega s) t + \sin(\omega s - \Delta \omega b) t \}$$

$$= \beta_a \cdot \sin(\omega s + \Delta \omega a) t + \beta_a \cdot \sin(\omega s - \Delta \omega a) t$$

$$+ \beta_b \cdot \sin(\omega s + \Delta \omega b) t + \beta_b \cdot \sin(\omega s - \Delta \omega b) t$$

$$S_{24} = -\beta_a \{ \sin(\Delta \omega a + \omega s) t - \sin(\omega s - \Delta \omega a) t \}$$

$$+ \beta_b \{ \sin(\Delta \omega b + \omega s) t - \sin(\omega s - \Delta \omega b) t \}$$

$$= -\beta_a \cdot \sin(\omega s + \Delta \omega a) t + \beta_a \cdot \sin(\omega s - \Delta \omega a) t$$

$$+ \beta_b \cdot \sin(\omega s + \Delta \omega b) t - \beta_b \cdot \sin(\omega s - \Delta \omega b) t$$

The signals  $S_{14}$  and  $S_{24}$  are then provided to an adding circuit 15 and added to each other. The resultant added signal expressed by the following equation is provided from the adding circuit 15.



$$S_{15} = S_{14} + S_{24}$$

$$= 2\beta a \cdot \sin(\omega_s - \Delta\omega a)t + 2\beta b \cdot \sin(\omega_s + \Delta\omega b)t$$

The added signal  $S_{15}$  has signal components as shown in FIG. 3B. This signal  $S_{15}$  is produced from the received signal  $S_r$  by replacing in the signal frequency conversion of the carrier frequency (angular frequency) with  $\omega_s$ . The signal  $S_{15}$  is the second IF signal having intermediate frequency  $f_s$ .

The second IF signal  $S_{15}$  is provided to the FM demodulating circuit 18 via a bandpass filter 16 as an IF filter and a limiter amplifier 17 and is then demodulated to the original audio signal. This audio signal is provided to the speaker 4 of a telephone receiver via an amplifier 19 and the terminal T14.

The above description is for the configuration and operation of the receiving circuit 10.

On the other hand, the transmitting circuit 40 processes an audio signal as an up channel FM signal. The divided signal  $S_{35}$  from the dividing circuit 35 is provided to the PLL 43 as a reference frequency signal. The dividing ratio  $N_{43}$  is provided to and set-up at a variable frequency dividing circuit (not shown in the drawings) of the PLL 43 via a terminal T18.

The up channel carrier frequency signal  $S_t$  received at the receiving circuit 10 that is paired with the down channel is provided from the VCO 431 of the PLL43.

The audio signal from the microphone 5 of the telephone receiver is provided to a low pass filter 42 via a terminal T15 and an amplifier 41 so that unnecessary band components are removed. An output signal from the low pass filter 42 is provided to the VCO 431 of the PLL 43 as an oscillating frequency control signal.

The audio signal from the low pass filter 42 is frequency-modulated at the VOC 431 to produce an FM signal  $S_t$  on the up channel signal that is paired with the down channel.

The FM signal  $S_t$  is extracted from the terminal T19 via the drive amplifier 44 and the output amplifier 45 and provided to the antenna 2.

The above description is for a description of the configuration and operation of the transmitting circuit 40.

If the FM receiver is a typical FM receiver, the intermediate frequency is high frequency such as 10.7 MHz. The intermediate filter should be therefore constructed from ceramic filters, so cannot be integrated into an IC.

However, with the aforementioned receiving circuit 10, the first IF signals  $S_{12}$  and  $S_{22}$  are baseband signals and the second intermediate frequency  $f_s$  is low frequency, such as 55kHz. The filters 13, 23 and 16 may therefore be constructed from active filters having resistors, capacitors and amplifiers. The receiving circuit 10, with the exception of the filter 3 and the oscillating coil (not

shown in the drawings) of the VCO311, can therefore be formed into an IC. Likewise, the transmitting circuit 40 can be formed into an IC.

The whole of the receiving circuit 10 and the transmitting circuit 40 shown in FIG. 1 and FIG. 2 can therefore be formed into a single monolithic IC.

In FIG. 1 and FIG. 2, this IC is used for a portable unit. However, connecting the terminals T14 and T15 to the 4 line/2 line conversion circuit and replacing the frequency-dividing ratios N31 with N43 allow the IC to be usable in the base unit. In this case, receiving of the up channel signal is carried out by the receiving circuit 10 and transmitting of the down channel signal is performed by the transmitting circuit 20.

This IC1 is therefore usable in both a base unit and a portable unit.

The VCO's 311 and 431 of the PLL's 31 and 43 are, as shown for example in FIG. 4, usually configured with Colpitts oscillating circuits.

In such circuit, the base of a transistor Q1 is connected to a power supply terminal T2 via a resistor R1. Capacitors C1 and C2 are connected between the base and ground. A capacitor C3 and a coil L1 are connected in series between this base and ground, as are a capacitor C4 and a variable capacitance diode D1.

Further, the collector of the transistor Q1 is

connected to a terminal T2 and a capacitor C5 is connected between this collector and ground. The emitter of the transistor Q1 is grounded via a resistor R2 and connected to the node of the capacitors C1 and C2. A control voltage VC is supplied to the variable capacitance diode D1 via the terminal T1 and a resistor R3.

The control voltage VC to the VCO311 is an error voltage indicating an oscillated frequency error, while the control voltage to the VCO431 is a voltage that is the sum of an error voltage indicating the oscillated frequency error and an audio signal voltage for modulation.

In this case, the circuit for determining the oscillated frequency of the VCO's 311 and 431 is simply represented by the equivalent circuit in FIG. 5A. Assuming as follows:

CS : minimum capacitance of the variable capacitance diode D1

$\Delta C$  : increase in capacitance of the diode D1 due to the control voltage VC ( $\Delta C \geq 0$ )

$\omega$  : oscillated frequency (carrier frequency) of circuits 311 and 431, and

$\omega_0$  : the oscillating frequency of the circuits 311 and 431 when  $\Delta C = 0$ .

The oscillated frequency  $\omega$  becomes:

$$\omega = \omega_0 [CS(1 + \Delta C / (C_3 + CS))$$

$$/ (CS + \Delta C)]^{**0.5}$$

$$\omega_0 = 1 / \{L_1 \cdot C_3 \cdot CS / (C_3 + CS)\}^{**0.5}$$

(where  $X^{**0.5}$  indicates X to the power of 1/2).

It therefore follows that the oscillating signal S31 or the FM signal St is outputted from the transistor Q1.

At this time,

$\Delta\omega$  : the variation in the oscillated frequency  $\omega$ ,

then,

$$\Delta\omega = C / (2CS) \cdot \omega_0 / (1 + CS/C3).$$

That is, when the variation  $\Delta C$  in the capacitance of the variable capacitance diode D1 is fixed, the variation  $\Delta\omega$  becomes larger as the oscillating frequency  $\omega_0$  becomes higher.

In order to avoid this, it is necessary to make the capacitance CS larger in proportion to the oscillating frequency  $\omega_0$ .

Further, the strength of the oscillated signal (FM signal) from the VCO's 311 and 431 is determined in accordance with the oscillation start conditions. Obtaining these oscillating conditions from the equivalent circuit in FIG. 5B for the sake of simplicity, then,

$$CS1 = CS2$$

$$= 2CS$$

and taking the easiest conditions for oscillation:

$g_m$  : mutual conductance of the transistor Q1.

This is proportional to the collector current of the transistor Q1.

Taking,

Q : Q of tuning circuit, then

$$g_m \geq 2\omega_0 C S / Q.$$

Therefore, if the  $Q$  of the tuning circuit is fixed, it is necessary to make the mutual conductance  $g_m$  larger in proportion to the oscillated frequency  $\omega_0$ .

It is also necessary for the mutual conductance  $g_m$  to be made larger when the capacitance  $C S$  is made larger in proportion to the oscillated frequency  $\omega_0$  so that the variation  $\Delta\omega$  does not become large regardless of the increase of the oscillated frequency. That is, when the oscillating frequency  $\omega_0$  is high, it is necessary to increase the mutual conductance  $g_m$ .

To use the IC1 in both the base unit and the portable unit, the oscillated frequencies for the VCO's 311 and 431 are determined as follows:

when the IC1 is used in a portable unit;

the oscillated frequency of the VCO311 is at 254 MHz band (down channel) and

the oscillated frequency of the VCO431 is at 381 MHz band (up channel)

and when the IC1 is used in a base unit;

the oscillated frequency of the VCO311 is at 381 MHz band (up channel) and

the oscillated frequency of the VCO431 is at 254 MHz band (down channel).

Thus, the frequency relationships are reversed in

individual units.

Therefore, when the IC1 is made usable in both the base unit and the portable unit, the VCO's 311 and 431 have to be able to oscillate at both the 254MHz and the 381MHz bands.

However, if the oscillating frequency of the VCO's 311 and 431 is made high, then the mutual conductance of the transistor Q1 increases. Otherwise, the oscillating conditions is not fulfilled so that an oscillated signal is not produced.

The present invention resolves such problems.

In order to resolve these problems, in the present invention a transmitting and receiving apparatus configured in superheterodyne form includes a receiving circuit and a transmitting circuit. The receiving circuit is for receiving a signal on one channel of a predetermined pair of channels and the transmitting circuit is for transmitting a signal on the other channel of the predetermined pair of channels. The receiving circuit has a first voltage-controlled oscillator, including a first oscillating transistor and generating a local oscillated signal used for receiving the signal on the one of the channels, and a first current control means for controlling a collector current of the first oscillating transistor. The transmitting circuit has a second voltage-controlled

oscillator including a second oscillating transistor and generating a signal of the frequency of the other channel, and a second current control means for controlling a collector current of the second oscillating transistor. One of the first and second current control means corresponding to one of the first and the second voltage controlled oscillator generating a signal with higher one of the frequencies of the predetermined pair of channels increases the collector current of the corresponding one of the first and the second oscillating transistors.

Further, the first current control means includes a first resistor and a series circuit of a second resistor and a first switch means connected in parallel between the emitter of the first oscillating transistor and the ground, and the second current control means includes a third resistor and a series circuit of a fourth resistor and a second switch means connected in parallel between the emitter of the second oscillating transistor and the ground.

Moreover, the first switch means and the second switch means may be switching transistors and the first and second voltage-controlled oscillators may be Colpitts oscillators.

Still further, according to the present invention, in a cordless telephone system in which a base unit and a portable unit both have transmitting circuits and receiving circuit configured in superheterodyne form and the base



unit and the portable unit transmit and receive signals therebetween using a predetermined pair of channels, each of the receiving circuits of the base unit and portable unit has a first voltage-controlled oscillator including a first oscillating transistor and generating a local oscillated signal used for receiving a signal on the partner's transmitting channel of the predetermined pair of channels, and a first current control means for controlling a collector current of the first oscillating transistor. Further, each of the transmitting circuits of the base unit and portable unit has a second voltage controlled oscillator including a second oscillating transistor and generating a signal with a frequency of a transmitting channel different from a frequency of the partner's transmitting channel of the predetermined pair of channels, and a second current control means for controlling a collector current of the second oscillating transistor. At each of the base unit and the portable unit, one of the first and the second current control means corresponding to one of the first and the second voltage controlled oscillators generating a signal with higher one of the frequencies of the predetermined pair of channels increases a collector current of the corresponding one of the first and the second oscillating transistors.

The present invention will be more clearly understood from the following description, given by way of example only, with reference to the accompanying drawings in which:

FIG. 1 is a system diagram showing an example of a

part of a portable unit circuit;

FIG. 2 is a system diagram showing a continuation of the example of FIG. 1;

FIG. 3A and FIG. 3B are a graph illustrating the operation of the circuit of FIG. 1 and FIG. 2;

FIG. 4 is a system diagram illustrating this invention;

FIG. 5A and FIG. 5B are a diagram illustrating the operation of the circuit of FIG. 4;

FIG. 6 is a connection diagram showing an example of this invention;

In FIG. 6, VCO311 is configured as being described with reference to FIG. 4, and a resistor R11 and the collector and the emitter of the transistor Q11 are connected in series between the emitter of the transistor Q1 and ground. The VCO 431 also has the same configuration.

However, in this case, as shown by the surrounding dot line, the portion with the exclusion of the elements C1 to C3 and L1 is formed within an IC1 and the elements C1 to C3 and L1 are attached to the IC1 via an external terminal.

Further, an external terminal T21 is connected to the base of the transistor Q11 for the switch operation of the VCO311 via a resistor R12 and also connected to the base of the transistor Q11 for the switch operation of the VCO431

via an inverter Q12 and a resistor R13.

Moreover, as shown by the dashed line, the terminal T21 is grounded when the IC1 is used in the portable unit, and is provided with the power supply voltage when used in the pass unit.

With such configuration, at a portable unit, the transistor Q11 of the VCO311 turns off since the terminal T21 is grounded. Thus, the collector current of the transistor Q1 of the VCO311 is determined by the resistors R1 and R2. The mutual conductance  $g_m$  corresponds to the magnitude of this collector current. The VCO311 therefore oscillates at an appropriate intensity by selecting resistances of the resistors R1 and R2 beforehand to produce an oscillated signal.

Further, the transistor Q11 of the VCO431 is turned on so that the resistor R11 is connected in parallel with the resistor R2 since the terminal T21 is grounded. The collector current of the transistor Q1 of the VCO431 is determined by the resistors R1, R2 and R11 and becomes larger than the collector current of the transistor Q1 for the VCO311. The mutual conductance  $g_m$  therefore becomes larger than the mutual inductance  $g_m$  of the transistor Q1 of the VCO311.

The VCO431 therefore oscillates to produce the oscillated signal (FM signal) at an appropriate intensity by selecting resistances of the resistor R11 beforehand

even if the oscillated frequency of the VCO311 is made high.

Further, at the base unit, since the terminal T21 is up to the power supply voltage, the transistor Q11 of the VCO311 is turned on and the transistor Q11 of the VCO431 is turned off. This relationship is opposite to that at the portable unit. Therefore, in this case also, VCO311 and VCO431 oscillate with appropriate intensity in respective frequency bands to produce the oscillation signals.

The VCO311 and VCO431 produce the oscillation signals with the appropriate intensity in both the portable unit and the base unit. The IC1 may therefore be commonly used for either the portable unit or the base unit.

Further, the collector current can be made smaller at the transistor Q1 of one of the VCO's 311 and 431 of which oscillated frequency is lower than that at the transistor Q1 of the other, so that the current consumed may therefore be made small.

This invention has been applied to a low power-type cordless telephone in the above description. However, this invention is suitable for transmitting/receiving device having dual channels. Further, at the VCO431, two variable capacitance diodes may be provided such that an error voltage indicative of the frequency error is provided to one of the diodes and an audio signal voltage is provided to the other.

According to this invention, in the case of use in a base unit or a portable unit of a cordless telephone, an oscillated signal with an appropriate strength is produced and the IC1 is commonly used in both the base unit or the portable unit.

Further, the collector current can be made smaller at the transistor Q1 of one of the VCO's 311 and 431 of which oscillated frequency is lower than that at transistor Q1 of the other, so that the current consumed may therefore be made small.

While the invention has been particularly shown and described with reference to the preferred embodiment thereof, it will be understood by those skilled in the art that the other changes in form and details can be made therein without departing from the spirit and scope of the invention.

### CLAIMS

1. A transmitting and receiving apparatus configured in superheterodyne form, said apparatus including a receiving circuit for receiving a signal on one channel of a predetermined pair of channels and a transmitting circuit for transmitting a signal on the other channel of the predetermined pair of channels, the improvement wherein

said receiving circuit has a first voltage-controlled oscillator which includes a first oscillating transistor and is for generating a local oscillated signal used for receiving the signal on the one of the channels, and a first current control means for controlling a collector current of the first oscillating transistor,

said transmitting circuit has a second voltage-controlled oscillator which includes a second oscillating transistor and is for generating a signal of the frequency of the other channel, and a second current control means for controlling a collector current of the second oscillating transistor, and

one of the first and the second current control means which corresponds to one of the first and the second voltage controlled oscillators that generates a signal with higher one of the frequencies of the predetermined pair of channels is for increasing the collector current of the corresponding one of the first and the second oscillating transistors.

2. A transmitting and receiving apparatus according to claim 1, wherein the first current control means includes a first resistor and a series circuit of a second resistor and a first switch means connected in parallel between the emitter of the first oscillating transistor and the ground, and the second current control means includes a third resistor and a series circuit of a fourth resistor and a second switch means connected in parallel between the emitter of the second oscillating transistor and the ground.

3. A transmitting and receiving apparatus according to claim 2, wherein the first switch means and the second switch means are switching transistors.

4. A transmitting and receiving apparatus according to claims 1, 2 or 3 wherein the first and second voltage-controlled oscillators are Colpitts oscillators.

5. A cordless telephone system with a base unit and the portable unit both having transmitting circuits and receiving circuits configured in superheterodyne form, wherein the base unit and the portable unit transmit and receive signals therebetween using a predetermined pair of channels, the improvement wherein

each of the receiving circuits of the base unit and portable unit has a first voltage-controlled oscillator including a first oscillating transistor and is for generating a local oscillated signal used for receiving a signal on the

partner's transmitting channel of the predetermined pair of channels, and a first current control means for controlling a collector current of the first oscillating transistor, and

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each of the transmitting circuits of the base unit and portable unit has a second voltage controlled oscillator including a second oscillating transistor and is for generating a signal with a frequency of a transmitting channel different from a frequency of the partner's transmitting channel of the predetermined pair of channels, and a second current control means for controlling a collector current of the second oscillating transistor, and

at each of the base unit and the portable unit, one of the first and the second current control means which corresponds to one of the first and the second voltage-controlled oscillators that generates a signal with higher one of the frequencies of the predetermined pair of channels is for increasing a collector current of the corresponding one of the first and the second oscillating transistors.

6. A transmitting and receiving apparatus constructed and arranged substantially as hereinbefore described with reference to and as illustrated by Figs. 4 to 6 of the accompanying drawings.

7. A cordless telephone system constructed and arranged substantially as hereinbefore described with reference to and as illustrated by Figs. 4 to 6 of the accompanying drawings.





# The Patent Office

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Application No: GB 9526443.8  
Claims searched: ALL

Examiner: Mr.SAT SATKURUNATH  
Date of 19 March 1996  
search:

## Patents Act 1977 Search Report under Section 17

### Databases searched:

UK Patent Office collections, including GB, EP, WO & US patent specifications, in:

UK CI (Ed.O): H3A: AB; H3R:RFMA

Int CI (Ed.6): H04B, H03B, H03C

Other: Online: WPI, JAPIO, INSPEC, EDOC

### Documents considered to be relevant:

Category	Identity of document and relevant passage	Relevant to claims
Y	WO 93/16530 A1 SONY - see especially figure 2	1
Y	US 4380827 ZENITH - see especially figures 3-6 and columns 3 and 4	1

X Document indicating lack of novelty or inventive step  
Y Document indicating lack of inventive step if combined with one or more other documents of same category.

& Member of the same patent family

A Document indicating technological background and/or state of the art.  
P Document published on or after the declared priority date but before the filing date of this invention.  
E Patent document published on or after, but with priority date earlier than, the filing date of this application.

